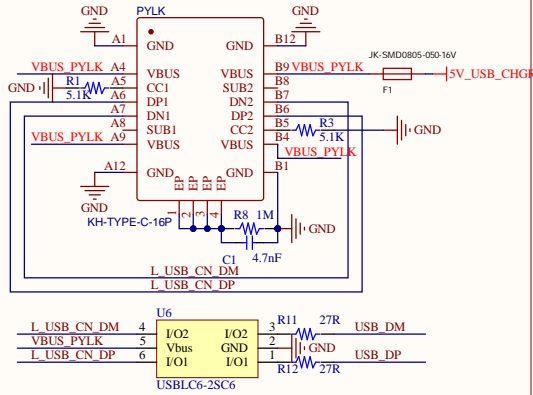
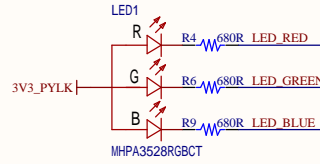


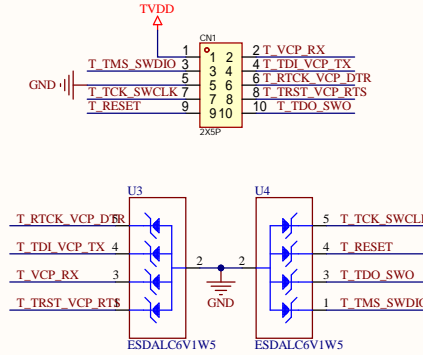
## USB



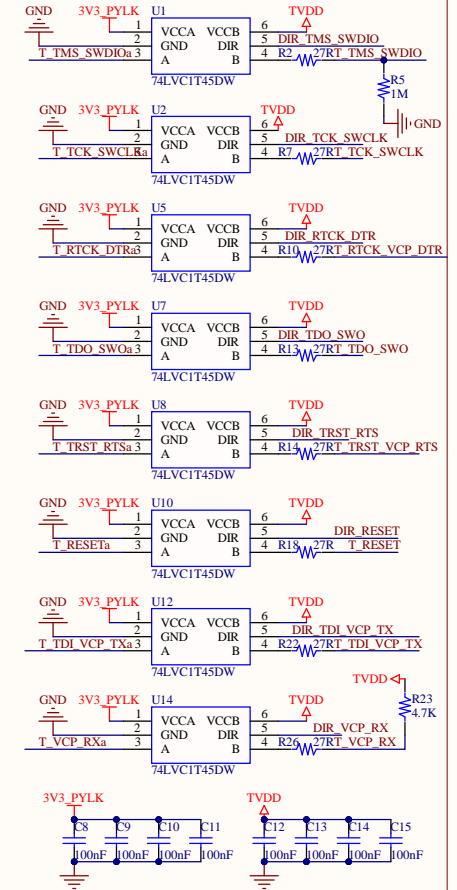
## LED



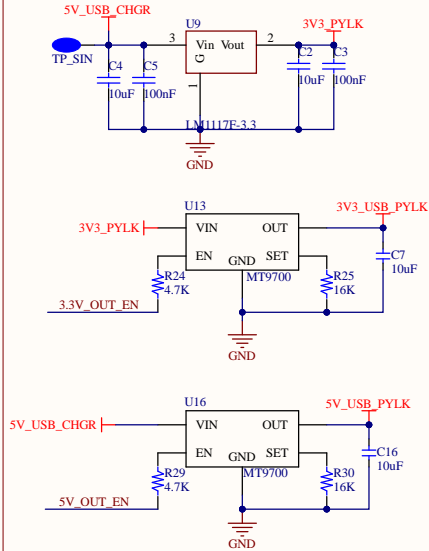
## Debug Connector



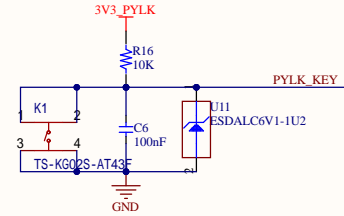
## Level Shift



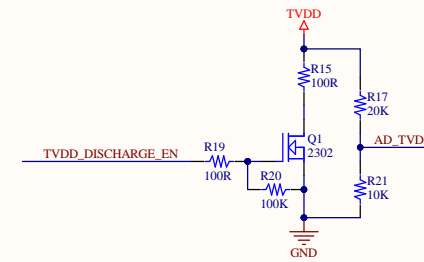
## Power



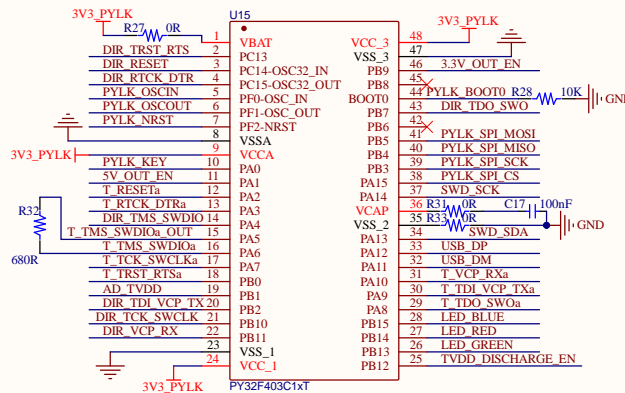
## Key



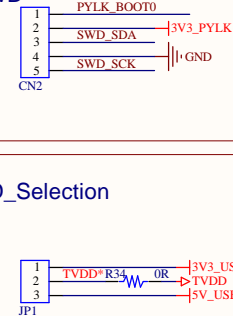
## ADC&Electric discharge



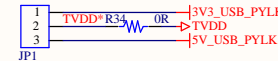
## MCU



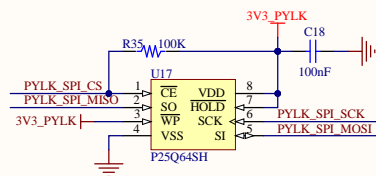
## SWD



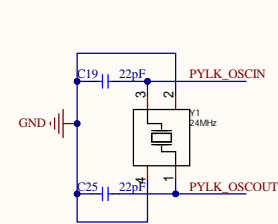
## TVDD\_Selection



## Flash



## OSC



When the DIR pin is set to logic high (1), the data direction is from VCCA to VCCB.  
When the DIR pin is set to logic low(0), the data direction is from VCCB to VCCA

T\_TMS\_SWDIO >> T\_TMS\_SWDIO  
T\_TCK\_SWCLK >> T\_TCK\_SWCLK  
T\_RESET >> T\_RESET  
T\_TDO\_SWO >> T\_TDO\_SWO  
T\_VCP\_RX >> T\_VCP\_RX  
T\_TDI\_VCP\_TX >> T\_TDI\_VCP\_TX  
T\_RTCK\_VCP\_DTR >> T\_RTCK\_VCP\_DTR  
T\_TRST\_VCP\_RTS >> T\_TRST\_VCP\_RTS

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The diagram shows a green LED connected between the TVDD pin and the PA1 pin. The LED is labeled 'LED\_PA1' and 'GREEN'. A resistor labeled 'R54' with a value of '1K' is connected in series between the LED and the PA1 pin.

## USB & POWER

### Debug Connector

The diagram illustrates the Debug Connector circuit. It features a 5-pin header (CN3) connected to a microcontroller. The connections are as follows:

- Pin 1: PA13 (R37) and 0R T. TMS. SWDIO
- Pin 2: PA14 (R39) and 0R T. TCK. SWCLK
- Pin 3: NRST (R40) and 0R T. RESET
- Pin 4: GND
- Pin 5: GND

A red arrow points to the TVDD pin, which is connected to the header.

The diagram illustrates the BOOT\_OPTION circuit. A 3X2 Header is connected to a JP2 connector. The connections are as follows:

- Pin 1 of JP2 is connected to PB2R42 through a 10K resistor.
- Pin 2 of JP2 is connected to TVDD.
- Pin 3 of JP2 is connected to R43.
- Pin 4 of JP2 is connected to BOOT0 through a 10K resistor.
- Pin 5 of JP2 is connected to GND.
- Pin 6 of JP2 is connected to GND.

# FLASH

The diagram shows a P25064SH Flash memory chip (U19) connected to an 8051 microcontroller. The chip is connected to F\_VDD, F\_GND, F\_CS, F\_SO, F\_WP, and F\_HOLD. The 8051 is connected to PA2, PB10, PB1, PB0, PA7, and PA6. The diagram shows the internal connections and the external components like resistors and capacitors.

8051 Pin	Flash Pin	Flash Label	Flash Pin	8051 Pin
PA2	R48	F_VDD	8	8
PB10	R49	F_CS	1	2
PB1	R50	F_SO	2	3
PB0	R51	F_WP	3	4
PA7	R52	F_HOLD	7	5
PA6	R53	F_HOLD	7	6

# VDD\_Selection

# 5V\_PWR\_Selection

# USER\_KEY

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